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PATENT  
740756-1551

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:

Shunpei YAMAZAKI et al.

Application No.: 08/691,434

Filed: August 2, 1996

For: METHOD FOR FABRICATING  
SEMICONDUCTOR DEVICES  
AND APPARATUS FOR  
PROCESSING A SEMICONDUCTOR



Art Unit: 2822

Examiner: M. Wilczewski

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on 01-29-01

*Deborah Thurman*

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patent  
Washington, D.C. 20231

January 29, 2001

Sir:

In accordance with the provisions of 37 C.F.R. § 1.56 and 37 C.F.R. §§ 1.97-1.99, it is requested that the references listed on the attached PTO-1449 form be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. § 1.98(a).

The Commissioner has hereby authorized to charge fees under 37 C.F.R. § 1.16, §§ 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. **A duplicate copy of this sheet is attached.**

Respectfully submitted,  
NIXON PEABODY LLP

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